

PATENT					
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** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-030068 02/03/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 740819-725	
TITLE : Method for fabricating semiconductor device					
U.S. DEPT. OF COM. PAT. & TM. PTO-436 (Rev. 12-01)					

<input type="checkbox"/> TERMINAL DISCLAIMER	Primary Examiner	Application Examiner
	PREPARED FOR ISSUE	
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